



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 2128 )  
)  
Examiner: K. Gebresilassie )  
)  
Applicant(s): J. G. Walacavage et al. )  
)  
Serial No.: 09/966,121 )  
)  
Filing Date: September 28, 2001 )  
)  
For: METHOD OF EMULATING )  
MACHINE TOOL BEHAVIOR FOR )  
PROGRAMMABLE LOGIC CONTROLLER )  
LOGICAL VERIFICATION SYSTEM )

**AMENDMENT  
UNDER 37 C.F.R. 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

This is in response to the Office Action dated May 14, 2008, regarding the above-identified application.

**CERTIFICATE OF MAILING:** (37 C.F.R. 1.8) I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service with sufficient postage as First Class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on July 14, 2008, by [Signature]  
Daniel H. Bliss